

OCT 21 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. 10/607,869
Filing Date June 27, 2003
Inventor Zhongze Wang
Assignee Micron Technology, Inc.
Group Art Unit 2812
Examiner Jennifer M. Kennedy
Attorney Docket No. MI22-2343
Customer No. 021567
Title: Method of Forming Silicon-on-Insulator Comprising Integrated Circuitry

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

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Dated: 10/21/04By: Pat Palmer

Pat Palmer

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Application Number	10/607,669
Filing Date	June 27, 2003
First Named Inventor	Zhongze Wang
Art Unit	2812
Examiner Name	Jennifer M. Kennedy
Attorney Docket Number	MI22-2343

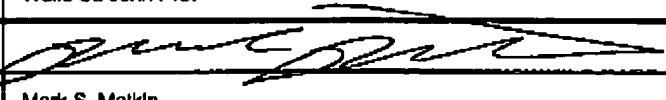
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Firm Name	Wells St. John P.S.		
Signature			
Printed name	Mark S. Matkin		
Date	10/21/04	Reg. No.	32,266

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STATEMENT OF THE SUBSTANCE OF THE INTERVIEW

To: Commissioner for Patents
ATTENTION: Examiner Jennifer M. Kennedy
Group Art Unit 2812
P. O. Box 1450
Alexandria, VA 22313-1450

VIA FACSIMILE

From: Mark Matkin (Tel. 509-624-4276; Fax 509-838-3424)
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An interview was conducted between the undersigned and Examiner Kennedy on October 18, 2004.

The undersigned emphasized those amendments and arguments pertaining thereto in the Response filed on September 23, 2004. Specifically with respect to claim 49, it was asserted that the added limitation of the insulator layer being formed at least by forming a first silicon dioxide layer on a first substrate, forming a second silicon dioxide layer on a second substrate and joining the first substrate to the second substrate was not taught or suggested by the applied art in combination with

the other claim 49 limitations. The Examiner agreed generally that such amendments defined patentable subject matter, subject to further searching and consideration. The undersigned also asserted that the withdrawn claims should now come back into this application, and be allowed.

Respectfully submitted,

Dated:

10-21-04

By:



Mark S. Matkin
Reg. No. 32,268